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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TESHIROGI, Kazuo et al.**

Group Art Unit: **2818**

Serial No.: **10/708,067**

Examiner: **Thao P. LE**

Filed: **February 6, 2004**

P.T.O. Confirmation No.: 2066

**FOR: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND A
METHOD FOR FIXING THE SEMICONDUCTOR DEVICE USING SUBSTRATE
JIG**

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 24, 2004

Sir:

In response to the Office Action dated **August 25, 2004**, entry of the following amendments and remarks, further examination, and reconsideration are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.